	Туре	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L5		(voltage divider) and (switch\$1 or pass-gate\$1 ot pass adj gate\$1 or transission adj gate\$1)	USPA T	2004/07/2 3 13:53
2	BRS	L6	20605	memory and (dram or ram) and (rom or eprom) and decoder	1	2004/07/2 3 13:55
3	BRS	L7	7733	5 and 6	1	2004/07/2 3 13:56
4	BRS	L8	234	327/\$.ccls. and 7		2004/07/2 3 13:57